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## **RG12864B-TIW-V**

## **SPECIFICATION**

## **CUSTOMER:**

**APPROVED BY** 

**PCB VERSION** 

DATE

FOR CUSTOMER USE ONLY

SALES BY	APPROVED BY	CHECKED BY	PREPARED BY

ISSUED DATE:



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## 1. Revision History

	DATE	VERSION	REVISED PAGE NO.	Note
	2010/02/08	1		First issue
			OP-	
		1100 A		
2				



### 2. General Specification

The Features is described as follow:

- Module dimension: 75.0 x 52.7 x 8.9 (max.) mm<sup>3</sup>
- View area: 60.0 x 32.6 mm<sup>2</sup>
- Active area: 55.0 x 27.48 mm<sup>2</sup>
- Number of dots: 128 x 64
- Dot size: 0.39 x 0.39 mm<sup>2</sup>
- Dot pitch: 0.43 x 0.43 mm<sup>2</sup>
- LCD type: FSTN Negative Transmissive
- Duty: 1/64
- View direction: 6 o'clock
- Backlight Type: LED White



## 3. Module Coding System

R	G	12864	В	-	Т	I	W	-	V
1	2	3	4	-	5	6	7	-	8

Item		Descripti	on
1	R ∶ Raystar C	•	
		C : Character Type,	
2	Display	G : Graphic Type	
3	Number of do	s : 128 x64 Dots	annanaile. Y
4	Serials code		and a second sec
		P : TN Positive, Gray	
		N : TN Negative,	
		G : STN Positive, Gray	A Data and
5	LCD	Y: STN Positive, Yellow Gr	een
		B: STN Negative, Blue	
		F : FSTN Positive	×
		T : FSTN Negative	
		A : Reflective, N.T, 6:00	K: Transflective, W.T,12:00
	Polarizer	D: Reflective, N.T, 12:00	1 : Transflective, U.T,6:00
	Type,	G: Reflective, W. T, 6:00	4 : Transflective, U.T.12:00
		J: Reflective, W. T, 12:00	C: Transmissive, N.T,6:00
6	Temperature	0 : Reflective, U. T, 6:00	F: Transmissive, N.T,12:00
	range,	3 : Reflective, U. T, 12:00	I : Transmissive, W. T, 6:00
	View 📈	B: Transflective, N.T,6:00	L: Transmissive, W.T,12:00
	direction	E: Transflective, N.T.12:00	2 : Transmissive, U. T, 6:00
	Can	H : Transflective, W.T,6:00	5 : Transmissive, U.T,12:00
		N : Without backlight	Y: LED, Yellow Green
		P : EL, Blue green	A : LED, Amber
7	Backlight	T : EL, Green	W : LED, White
1943		D : EL, White	O: LED, Orange
Karr	W.	F : CCFL, White	G : LED, Green
8	Special code	V: Built-in Negative Voltage	

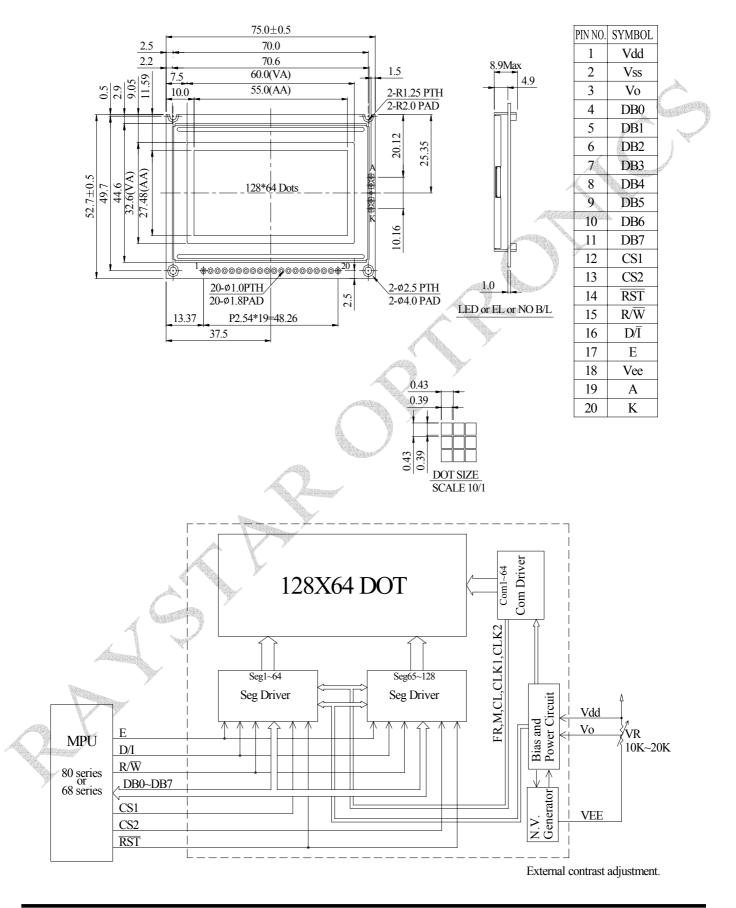


### 4. Interface Pin Function

Pin #	Symbol	Level	Description	
1	VDD	5.0V	Supply voltage for logic	
2	V <sub>SS</sub>	0V	Ground	A
3	Vo	(Variable)	Contrast Adjustment	Cat
4	DB0	H/L	Data bus line	
5	DB1	H/L	Data bus line	1 Marson
6	DB2	H/L	Data bus line 🛛 🗸 🛝	- Market
7	DB3	H/L	Data bus line	
8	DB4	H/L	Data bus line	
9	DB5	H/L	Data bus line	
10	DB6	H/L	Data bus line	
11	DB7	H/L	Data bus line	
12	CS1	L	Select Column 1~ Column 64	
13	CS2	L	Select Column 65~ Column 128	
14	RST	L	Reset signal	
15	R/W	H/L	H: Read (MPU←Module) , L: Write (MPU→Module)	
16	D/I	H/L	H: Data, L: Instruction	
17	E	Н	Enable signal	
18	Vee	—	Negative Voltage output	
19	A	—	LED +	
20	K		LED -	



## 5. Outline Dimension & Block Diagram





### 6. Display Control Instruction

The internal state of NT7108 is defined by Display Control Instruction, sent by MPU, shown in the table below.

Instruction	RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	Function		
Display on/off	L	L	L	L	н	н	н	Н	н	L/H	data is not affected. L:OFF, H:ON Sets the Y address in the Y address counter. Sets the X address at the X address register.		
Set address (Y address)	L	L	L	Н		Y	addres	ss (0-6	63)	•			
Set page (X address)	L	L	н	L	Н	Н	Н	Pa	age (O	-7)			
Display Start line (Z address)	L	L	н	н		Displa	ay star	tart line (0-63)			Indicates the display data RAM displayed at the top of the screen.		
Status read	L	н	Busy	L	On/ Off	Reset	L	L	L	L	Read status. BUSY L: Ready H: In operation ON/OFF L: Display ON H: Display OFF RESET L: Normal H: Reset		
Write display data	н	L		-	-	Write	data		-	-	Writes data (DB0: 7) into display data RAM. After writing instruction, Y address is increased by 1 automatically.		
Read display data	Н	н				Read	data				Reads data (DB0: 7) from display data RAM to the data bus.		

#### Instruction

#### Display ON/OFF

	10	Allow Roper							
RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
0	0	0	0	1	1	1	1	1	D

D gives the value of DB0. The Display Data appears as D is 1, and disappears as D is 0. The Display Data still remains in Display Data RAM as D is 0, though the data is not shown on the screen. The Display Data can reappear by switching D from 0 to 1.



#### **SET ADDRESS (Y ADDRESS)**

Ī	RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
	0	0	0	1	AC5	AC4	AC3	AC2	AC1	AC0

The Y addresses (AC0-AC5) of Display Data RAM are set in the Y address counter. Each address is set by instruction and incremented by 1 automatically by read or write operations.

#### SET PAGE (X ADDRESS)

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
0	0	1	0	1	1	1	AC2	AC1	AC0

The X address (AC0-AC2) of Display Data RAM is set in the X address counter. Writing or reading to or from MPU is executed in this specified page until the next page is set.

#### **DISPLAY START LINE (Z ADDRESS)**

							5080. s		
RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
0	0	1	1	AC5	AC4	AC3	AC2	AC1	AC0

Z addresses (AC0-AC5) of the Display Data RAM are set in the display start line register and displayed at the top of the screen. When the display duty cycle is 1/64 or others (1/32-1/64), the data of total line number of LCD screen are displayed from the line specified by display start line instruction.

#### STATUS READ

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
0	1	BUSY	0	ON/OFF	RESET	0	0	0	0

• BUSY

As BUSY is 1, the Chip is executing internal operation and can not accept any instruction. As BUSY is 0, the Chip is ready to accept any instruction.

#### • ON/OFF

As ON/OFF is 1, the display is OFF.

As ON/OFF is 0, the display is ON.

• RESET

When RESET is 1, the system is being initialized.

In this condition, no instructions can be accepted except for the Status Read instruction. As RESET is 0, initializing has finished and the system is in its normal operation condition.

#### WRITE DISPLAY DATA

R	5	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
1		0	D7	D6	D5	D4	D3	D2	D1	D0

The Write Display Data (D0-D7) are written into the Display Data RAM. Completing the writing instruction, Y address is increased by 1 automatically.

#### **READ DISPLAY DATA**

									· · · · · · · · · · · · · · · · · · ·	
[	RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
[	1	1	D7	D6	D5	D4	D3	D2	D1	D0

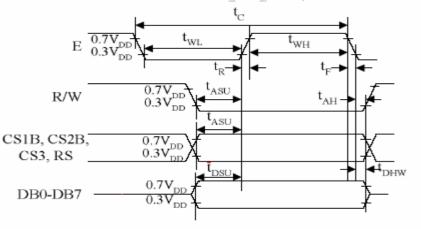
The Read Display Data (D0-D7) are read from the display data RAM. Completing the reading instruction, Y address is increased by 1 automatically.



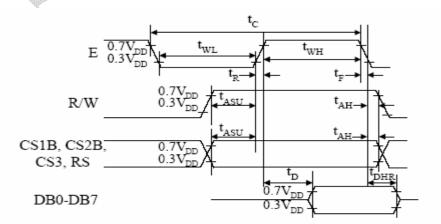
## 7. Timing Characteristics

alanga a

MPU Interface			<b>(T=25</b> ℃,	VDD=+5.0V±	0.5)	_
Characteristic	Symbol	Min	Тур	Мах	Unit	
E cycle	tcyc	1000	_	_	ns	and the second
E high level width	twhE	450			ns	
E low level width	twlE	450	—	—	ns	Harmon
E rise time	tr	—	—	25	ns	and a second
E tall time	tf	_	_	25	ns	
Address set-up time	tas	140	_		ns	
Address hold time	tah	10			ns	
Data set-up time	tdsw	140	_		ns	
Data delay time	tddr		Ada	320	ns	
Data hold time (write)	tdhw	10			ns	
Data hold time (read)	tdhr	20		_	ns	



#### MPU Write Timing



MPU Read Timing

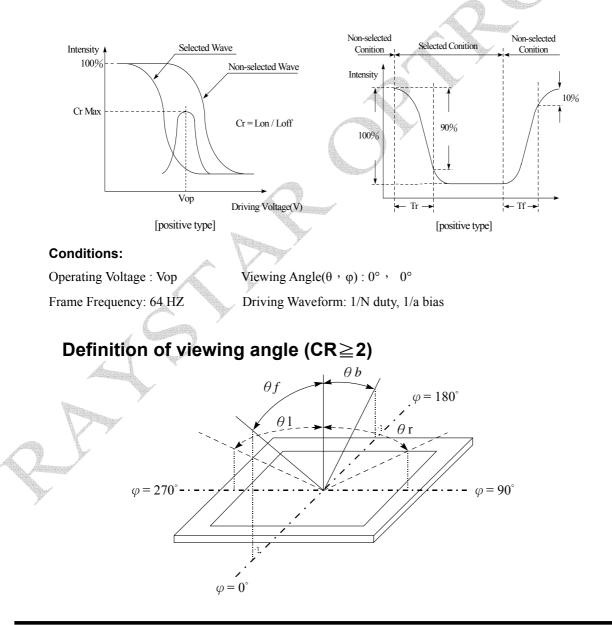


### 8. Optical Characteristics

ltem	Symbol	Condition	Min	Тур	Max	Unit	
View Angle	(V)θ	CR≧2	30	—	60	deg	and a
view / (rigie	(H)φ	CR≧2	-45		45	deg	5
Contrast Ratio	CR	_	_	5	Å		
Response Time	T rise	_	—	150	200	ms	P.
	T fall	_	_	150	200	ms	

#### Definition of Operation Voltage, Vop.

Definition of Response Time, Tr and Tf.





## 9. Absolute Maximum Ratings

ltem	Symbol	Min	Тур	Max	Unit	
Operating Temperature	T <sub>OP</sub>	-20	_	+70	°C	
Storage Temperature	T <sub>ST</sub>	-30	_	+80	°C	
Input Voltage	VI	0	_	V <sub>DD</sub>	V	
Supply Voltage For Logic	V <sub>DD</sub>	0		6.7	V	and the second se
Supply Voltage For LCD	VDD-V <sub>LCD</sub>	0	_	16.7	V	

### **10. Electrical Characteristics**

Item	Symbol	Condition	Min	Tvn	Max	Unit
nem	Symbol	Condition	INITI	Тур	IVIAX	Unit
Supply Voltage For		Á	) /			
Logic	$V_{DD}$ - $V_{SS}$		4.5	5.0	5.5	V
			A Barrow			
		Ta=-20℃	_	—	9.6	V
Supply Voltage For LCD	V <sub>DD</sub> -V <sub>0</sub>	<b>Ta=25</b> ℃	_	8.0		V
		<b>Ta=+70</b> ℃	7.6	—	—	V
Input High Volt.	VH		2.0		V <sub>DD</sub>	V
Input Low Volt.	VIL	_	0	—	0.8	V
Output High Volt.	V <sub>OH</sub>	_	2.4	—	V <sub>DD</sub>	V
Output Low Volt.	V <sub>OL</sub>	_	_	_	0.4	V
Supply Current	I <sub>DD</sub>	_	3.0	4.0	5.0	mA



### **11. Backlight Information**

### Specification

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT	TEST	CONDITION
Supply Current	I <sub>LED</sub>	43.2	48	60	mA	V=3.5V	Second Street Stre
Supply Voltage	v	3.4	3.5	3.6	V	-	
Reverse Voltage	VR			5	V	_	
Luminous Intensity	IV	116.8	146.2	_	cd/m <sup>2</sup>	l <sup>LED</sup> =48n	nA
Wave Length	λр	_		_	nm	l <sub>LED</sub> =48n	nA
Life Time	—	—	50K	—	hr.	l <sub>LED</sub> =48n	nA
Color	White	1	1	A standard			

Note:

The LED of B/L is drive by current only; drive voltage is for reference only. Drive voltage has to make driving current under safety area (current between minimum and maximum).

2.Drive from pin19,pin20

R R pin19 А B/L K pin20 LCM



### 12. Reliability

#### Content of Reliability Test (wide temperature, -20°c~70°C)

Environmental Test							
Test Item	Content of Test	Condition	Note				
High Temperature storage	Endurance test applying the high storage temperature for a long time.	80℃ 200hrs	2				
Low Temperature storage	Endurance test applying the high storage temperature for a long time.	-30℃ 200hrs	1,2				
High Temperature Operation	Endurance test applying the electric stress (Voltage & Current) and the thermal stress to the element for a long time.	70℃ 200hrs					
Low Temperature Operation		-20℃ 200hrs	1				
High Temperature/ Humidity Operation	The module should be allowed to stand at 60°C,90%RH max For 96hrs under no-load condition excluding the polarizer, Then taking it out and drying it at normal temperature.	60℃,90%RH 96hrs	1,2				
Thermal shock resistance	The sample should be allowed stand the following 10 cycles of operation	-20℃/70℃ 10 cycles	-				
Vibration test	Endurance test applying the vibration during transportation and using.	fixed amplitude: 15mm Vibration. Frequency: 10~55Hz. One cycle 60 seconds to 3 directions of X,Y,Z for Each 15 minutes	3				
Static electricity test	Endurance test applying the electric stress to the terminal.	VS=800V,RS= 1.5kΩ CS=100pF 1 time					

Note1: No dew condensation to be observed.

Note2: The function test shall be conducted after 4 hours storage at the normal temperature and humidity after remove from the test chamber.

Note3: Vibration test will be conducted to the product itself without putting it in a container.



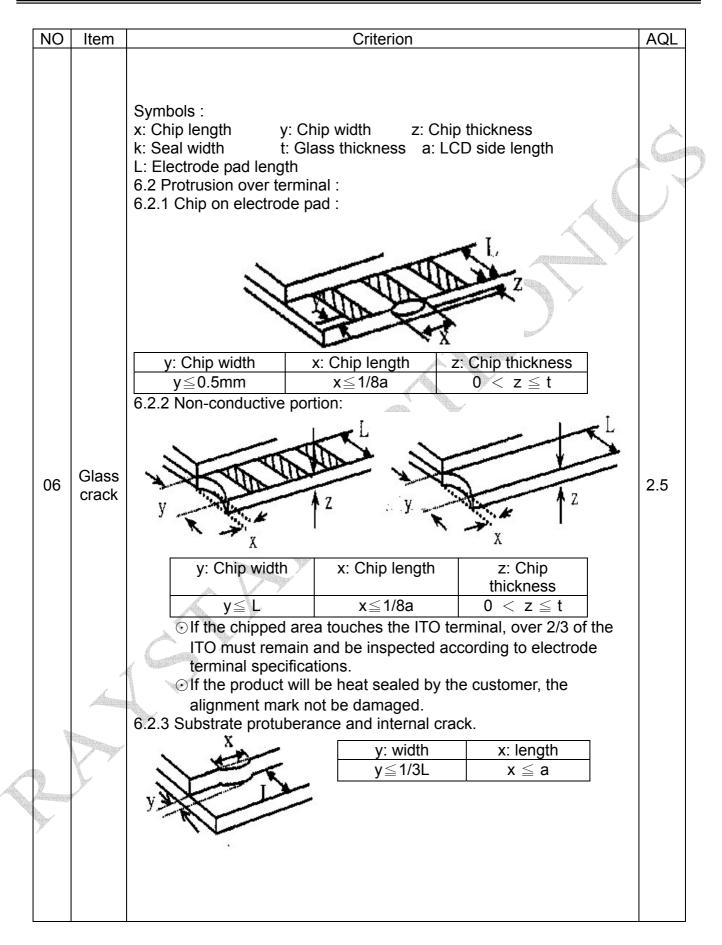
## **13. Inspection specification**

NO	Item			Criterion		AQL
01	Electrical Testing	defect. 1.2 Missing cha 1.3 Display mal 1.4 No function	racter, do function. or no dis sumption g angle do uct types.	play. exceeds product		0.65
02	Black or white spots on LCD (display only)	than three v 2.2 Densely spa 3mm	vhite or bl aced: No	ts on display $\leq 0.2$ ack spots present more than two spo		2.5
03	LCD black spots, white spots, contaminatio	3.1 Round type Φ=( x + y ) /				2.5
	n (non-display)	3.2 Line type :	(As follow Length	ring drawing) Width W≦0.02	Acceptable Q TY Accept no	2.5
	16		L≦3.0 L≦2.5 	$\begin{array}{c} 0.02\!<\!W\!\leq\!0.03\\ 0.03\!<\!W\!\leq\!0.05\\ 0.05\!<\!W \end{array}$	dense 2 As round type	2.5
04	Polarizer bubbles	If bubbles are v judge using bla specifications, i easy to find, mu check in specify direction.	ck spot not ust	Size Φ         Φ $\leq$ 0.20         0.20<Φ $\leq$ 0.50         0.50<Φ $\leq$ 1.00         1.00<Φ	Acceptable Q TY Accept no dense 3 2 0 3	2.5



NO	Item		Criterion		AQL
05	Scratches	Follow NO.3 LCD bla	ck spots, white spots,	contamination	
		<ul><li>k: Seal width t</li><li>L: Electrode pad leng</li><li>6.1 General glass chi</li></ul>		LCD side length	
06	Chipped	$\frac{z: Chip thickness}{Z \le 1/2t}$	y: Chip width Not over viewing area	x: Chip length x≦1/8a	2.5
00	glass	$1/2t < z \leq 2t$	Not exceed 1/3k	x≦1/8a	2.0
		⊙ If there are 2 or mo 6.1.2 Corner crack:	re chips, x is total leng	gth of each chip.	
		z: Chip thickness	y: Chip width	x: Chip length	
		Z≦1/2t	Not over viewing area	x≦1/8a	
A STATE OF STATE		1/2t <z≦2t< td=""><td>Not exceed 1/3k</td><td>x≦1/8a</td><td></td></z≦2t<>	Not exceed 1/3k	x≦1/8a	
an a	p.	$\odot$ If there are 2 or mo	re chips, x is the total	length of each chip.	







NO	Item	Criterion	AQL
07	Cracked glass	The LCD with extensive crack is not acceptable.	2.5
08	Backlight elements	<ul> <li>8.1 Illumination source flickers when lit.</li> <li>8.2 Spots or scratched that appear when lit must be judged. Using LCD spot, lines and contamination standards.</li> <li>8.3 Backlight doesn't light or color wrong.</li> </ul>	0.65 2.5 0.65
09	Bezel	<ul><li>9.1 Bezel may not have rust, be deformed or have fingerprints, stains or other contamination.</li><li>9.2 Bezel must comply with job specifications.</li></ul>	2.5 0.65
10	PCB · COB	<ul> <li>10.1 COB seal may not have pinholes larger than 0.2mm or contamination.</li> <li>10.2 COB seal surface may not have pinholes through to the IC.</li> <li>10.3 The height of the COB should not exceed the height indicated in the assembly diagram.</li> <li>10.4 There may not be more than 2mm of sealant outside the seal area on the PCB. And there should be no more than three places.</li> <li>10.5 No oxidation or contamination PCB terminals.</li> <li>10.6 Parts on PCB must be the same as on the production characteristic chart. There should be no wrong parts, missing parts or excess parts.</li> <li>10.7 The jumper on the PCB should conform to the product characteristic chart.</li> <li>10.8 If solder gets on bezel tab pads, LED pad, zebra pad or screw hold pad, make sure it is smoothed down.</li> <li>10.9 The Scraping testing standard for Copper Coating of PCB</li> <li>X * Y&lt;=2mm<sup>2</sup></li> </ul>	<ol> <li>2.5</li> <li>2.5</li> <li>0.65</li> <li>2.5</li> <li>0.65</li> <li>0.65</li> <li>2.5</li> <li>2.5</li> <li>2.5</li> <li>2.5</li> </ol>
		<u>X * 1&lt;=2mm</u>	2.5 2.5
11	Soldering	<ul> <li>11.1 No un-melted solder paste may be present on the PCB.</li> <li>11.2 No cold solder joints, missing solder connections, oxidation or icicle.</li> <li>11.3 No residue or solder balls on PCB.</li> <li>11.4 No short circuits in components on PCB.</li> </ul>	2.5 0.65



Item	Criterion	AQL
General appearance	<ul> <li>12.1 No oxidation, contamination, curves or, bends on interface Pin (OLB) of TCP.</li> <li>12.2 No cracks on interface pin (OLB) of TCP.</li> <li>12.3 No contamination, solder residue or solder balls on product.</li> <li>12.4 The IC on the TCP may not be damaged, circuits.</li> <li>12.5 The uppermost edge of the protective strip on the interface pin must be present or look as if it causes the interface pin to sever.</li> <li>12.6 The residual rosin or tin oil of soldering (component or chip component) is not burned into brown or black color.</li> <li>12.7 Sealant on top of the ITO circuit has not hardened.</li> <li>12.8 Pin type must match type in specification sheet.</li> <li>12.10 Product packaging must the same as specified on packaging specification sheet.</li> <li>12.11 Product dimension and structure must conform to product specification sheet.</li> </ul>	<ol> <li>2.5</li> <li>0.65</li> <li>2.5</li> <li>2.5</li> <li>2.5</li> <li>2.5</li> <li>0.65</li> <li>0.65</li> <li>0.65</li> <li>0.65</li> <li>0.65</li> </ol>
	General	<ul> <li>General appearance</li> <li>General appearance</li> <li>12.1 No oxidation, contamination, curves or, bends on interface Pin (OLB) of TCP.</li> <li>12.2 No cracks on interface pin (OLB) of TCP.</li> <li>12.3 No contamination, solder residue or solder balls on product.</li> <li>12.4 The IC on the TCP may not be damaged, circuits.</li> <li>12.5 The uppermost edge of the protective strip on the interface pin must be present or look as if it causes the interface pin to sever.</li> <li>12.6 The residual rosin or tin oil of soldering (component or chip component) is not burned into brown or black color.</li> <li>12.7 Sealant on top of the ITO circuit has not hardened.</li> <li>12.8 Pin type must match type in specification sheet.</li> <li>12.9 LCD pin loose or missing pins.</li> <li>12.10 Product packaging must the same as specified on packaging specification sheet.</li> <li>12.11 Product dimension and structure must conform to</li> </ul>

## 14. Precautions in use of LCD Modules

- 1. Avoid applying excessive shocks to the module or making any alterations or modifications to it.
- 2. Don't make extra holes on the printed circuit board, modify its shape or change the components of LCD module.
- 3. Don't disassemble the LCM.
- 4. Don't operate it above the absolute maximum rating.
- 5. Don't drop, bend or twist LCM.
- 6. Soldering: only to the I/O terminals.
- 7. Storage: please storage in anti-static electricity container and clean environment.



### **15. Material List of Components for RoHs**

1. RAYSTAR Optronics Co., Ltd. hereby declares that all of or part of products, including, but not limited to, the LCM, accessories or packages, manufactured and/or delivered to your company (including your subsidiaries and affiliated company) directly or indirectly by our company (including our subsidiaries or affiliated companies) do not intentionally contain any of the substances listed in all applicable EU directives and regulations, including the following substances.

Exhibit A : The Harmful Material List

Material	(Cd)	(Pb)	(Hg)	(Cr6+)	PBBs	PBDEs	
Limited Value	100 ppm	1000 ppm	1000 ppm	1000 ppm	1000 ppm	1000 ppm	
Above limited value is set up according to RoHS.							

- 2. Process for RoHS requirement :
  - (1) Use the Sn/Ag/Cu soldering surface; the surface of Pb-free solder is rougher than we used before.
  - (2) Heat-resistance temp. :

Reflow : 250°C, 30 seconds Max.

Connector soldering wave or hand soldering :  $320^{\circ}$ C, 10 seconds max.

(3) Temp. curve of reflow, max. Temp. :  $235\pm5^{\circ}$ ;

Recommended customer's soldering temp. of connector :  $280^{\circ}$ C, 3 seconds.



Page: 1

LCI Module Number :		Estimate Feedback Sheet
1 · Panel Specification :		
1. Panel Type :	Pass	□ NG ,
2. View Direction :	□ Pass	□ NG ,
3. Numbers of Dots :	□ Pass	□ NG ,
4. View Area :	□ Pass	□ NG ,
5. Active Area :	□ Pass	□ NG ,
6.Operating Temperature:	Pass	□ NG ,
7.Storage Temperature :	□ Pass	□ NG ,
8.Others :	•	
2 · Mechanical Specificat	ion :	
1. PCB Size :	🗆 Pass	🗆 NG ,
2.Frame Size :	□ Pass	□ NG ,
3.Materal of Frame :	□ Pass	□ NG ,
4.Connector Position :	□ Pass	🗆 NG ,
5.Fix Hole Position :	□ Pass	□ NG ,
6.Backlight Position :	□ Pass	□ NG ,
7. Thickness of PCB :	□ Pass	□ NG ,
8. Height of Frame to		□ NG ,
PCB :		
9.Height of Module :	🗆 Pass	_ NG ,
10.Others :	D Pass	□ NG ,
3 · <u>Relative Hole Size</u> :	A free de	
1.Pitch of Connector:	D Pass	□ NG ,
2.Hole size of	D Pass	□ NG ,
Connector :		
3.Mounting Hole size :	Pass	□ NG ,
4.Mounting Hole Type :	Pass	□ NG ,
5.Others :	Pass	□ NG ,
4 · Backlight Specification	<u>ı</u> :	
1.B/L Type :	□ Pass	□ NG ,
2.B/L Color :	□ Pass	🗆 NG ,
	ference for L	ED Type) : □ Pass □ NG ,
4.B/L Driving Current :	Pass	□ NG ,
5.Brightness of B/L :	Pass	□ NG ,
6.B/L Solder Method :	Pass	□ NG ,
7.Others:	□ Pass	□ NG ,



Page: 2

		Faye. 2
Module Number :		_
5 · Electronic Characteristic		
1.Input Voltage :	Pass	□ NG ,
2.Supply Current :	Pass	□ NG ,
3.Driving Voltage for LCD :	Pass	🗆 NG ,
4.Contrast for LCD :	Pass	□ NG ,
5.B/L Driving Method :	Pass	□ NG ,
6.Negative Voltage	Pass	□ NG ,
Output :		
7.Interface Function :	Pass	□ NG ,
8.LCD Uniformity :	□ Pass	□ NG ,
9.ESD test :	□ Pass	□ NG ,
10.Others :	Pass	□ NG ,
6 <u>Summary</u> :		
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Lauren and Star		
Sales signature :	•	Dete : / /
Customer Signature	•	Date : / /